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Bib Data Sheet

CONFIRMATION NO. 3926

SERIAL NUMBER 10/776,375	FILING DATE 02/11/2004  RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 09792909-5807
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

*none  
Loke*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN JP2003-043896 02/21/2003  
*Loke*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED.

\*\* 05/07/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	17	13	5
Verified and Acknowledged	<i>Steven Loke</i> <i>S.L.</i> Examiner's Signature Initials				

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## TITLE

Semiconductor device and method of manufacturing the same

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